

2MBI900VXA-120E-54

IGBT Modules

IGBT MODULE (V series) 1200V / 900A / 2 in one package

■ Features

- High speed switching
- Voltage drive
- Low Inductance module structure

■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply
- Industrial machines, such as Welding machines



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Maximum ratings	Units	
Inverter	Collector-Emitter voltage	V_{CES}	1200	V	
	Gate-Emitter voltage	V_{GES}	± 20	V	
	Collector current	I_c	Continuous	$T_c=25^\circ\text{C}$ 1200 $T_c=100^\circ\text{C}$ 900	A
			$I_{c\text{ pulse}}$	1ms	
		$-I_c$		900	
		$-I_{c\text{ pulse}}$	1ms	1800	
Collector power dissipation	P_c	1 device	5100	W	
Junction temperature	T_j		175	$^\circ\text{C}$	
Operating junction temperature (under switching conditions)	T_{jop}		150		
Case temperature	T_c		150		
Storage temperature	T_{stg}		-40 ~ +150		
Isolation voltage	between terminal and copper base (*1) between thermistor and others (*2)	V_{iso}	AC : 1min.	4000	VAC
	Mounting	-	M5	6.0	N m
Screw torque (*3)	Main Terminals	-	M8	10.0	
	Sense Terminals	-	M4	2.1	

Note *1: All terminals should be connected together during the test.

Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note *3: Recommendable Value : Mounting 3.0 ~ 6.0 Nm (M5)
 Recommendable Value : Main Terminals 8.0 ~ 10.0 Nm (M8)
 Recommendable Value : Sense Terminals 1.8 ~ 2.1 Nm (M4)

● Electrical characteristics (at $T_j=25^\circ\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Zero gate voltage collector current	I_{CES}	$V_{GE} = 0V, V_{CE} = 1200V$	-	-	8.0	mA	
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	1600	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V, I_c = 900mA$	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal) (*4)	$V_{GE} = 15V$ $I_c = 900A$	$T_j=25^\circ\text{C}$	-	1.85	2.30	V
			$T_j=125^\circ\text{C}$	-	2.15	-	
	$T_j=150^\circ\text{C}$		-	2.20	-		
	$V_{CE(sat)}$ (chip)		$T_j=25^\circ\text{C}$	-	1.75	2.20	
			$T_j=125^\circ\text{C}$	-	2.05	-	
			$T_j=150^\circ\text{C}$	-	2.10	-	
Internal gate resistance	$R_{G(int)}$	-	-	1.19	-	Ω	
Input capacitance	C_{ies}	$V_{CE} = 10V, V_{GE} = 0V, f = 1MHz$	-	83	-	nF	
Turn-on time	t_{on}	$V_{CC} = 600V$	-	1000	-	nsec	
	t_r	$I_c = 900A$	-	400	-		
	$t_{(f)}$	$V_{GE} = \pm 15V$	-	150	-		
	t_{off}	$R_G = 1.6\Omega$	-	1200	-		
Turn-off time	t_r	$L_s = 70nH$	-	150	-	nsec	
	t_{tr}		-	200	-		
Forward on voltage	V_F (terminal) (*4)	$V_{GE} = 0V$ $I_F = 900A$	$T_j=25^\circ\text{C}$	-	1.90	2.35	V
			$T_j=125^\circ\text{C}$	-	2.05	-	
	$T_j=150^\circ\text{C}$		-	2.00	-		
	V_F (chip)		$T_j=25^\circ\text{C}$	-	1.80	2.25	
			$T_j=125^\circ\text{C}$	-	1.95	-	
			$T_j=150^\circ\text{C}$	-	1.90	-	
Reverse recovery time	t_{rr}	$I_F = 900A$	-	200	-	nsec	
Thermistor Resistance	R	T = 25 $^\circ\text{C}$	-	5000	-	Ω	
		T = 100 $^\circ\text{C}$	465	495	520		
B value	B	T = 25/50 $^\circ\text{C}$	3305	3375	3450	K	

Note *4: Fuji defined V_{CE} value of terminal by using Sense C1 and Sense C2E1 for Upper arm and Sense C2E1 and Sense E2 for Lower arm.

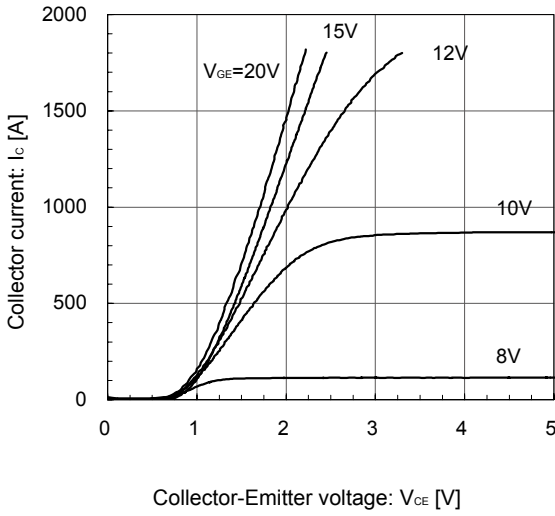
● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance(1device)	R _{th(j-c)}	Inverter IGBT	-	-	0.030	°C/W
		Inverter FWD	-	-	0.054	
Contact thermal resistance (1device) (*5)	R _{th(c-f)}	with Thermal Compound	-	0.00625	-	

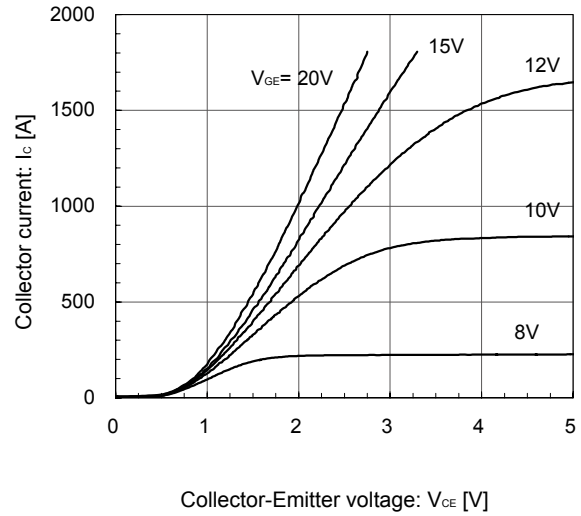
Note *5: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)

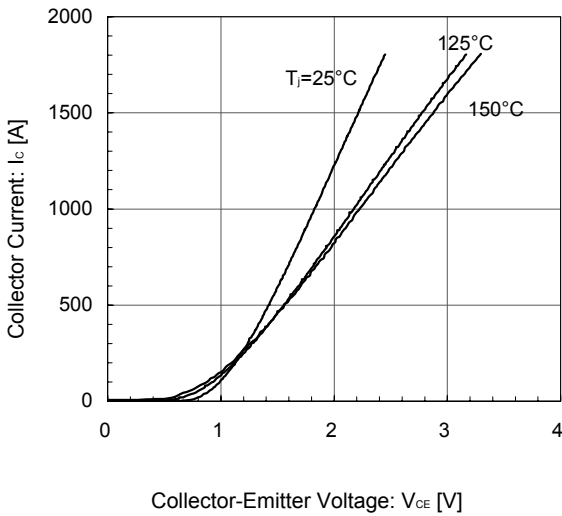
[INVERTER]
Collector current vs. Collector-Emittor voltage (typ.)
 $T_j = 25^\circ\text{C}$ / chip



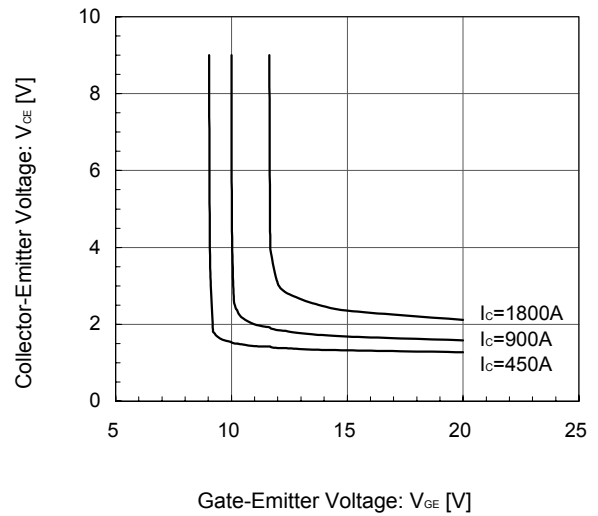
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Collector current vs. Collector-Emittor voltage (typ.)
 $T_j = 150^\circ\text{C}$ / chip



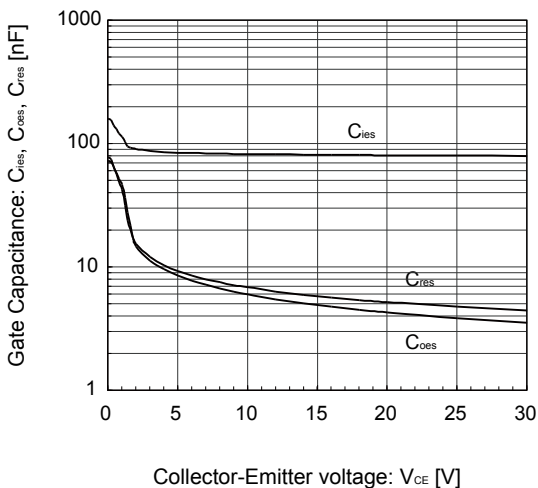
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Collector current vs. Collector-Emittor voltage (typ.)
 $V_{GE} = 15\text{V}$ / chip



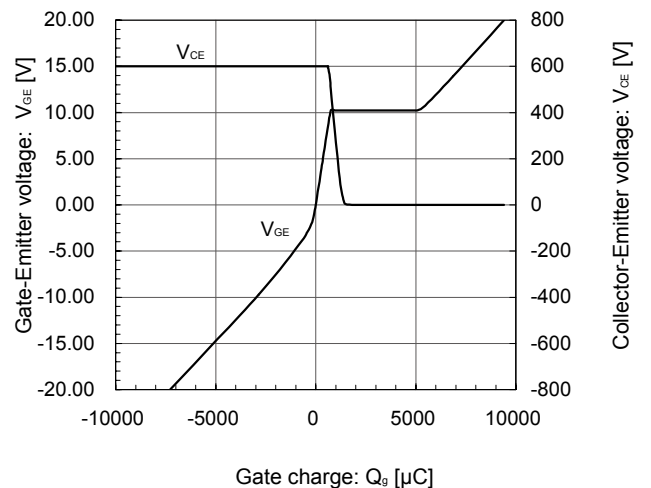
[INVERTER]
Collector-Emittor voltage vs. Gate-Emittor voltage (typ.)
 $T_j = 25^\circ\text{C}$ / chip



[INVERTER]
Gate Capacitance vs. Collector-Emittor Voltage (typ.)
 $V_{GE} = 0\text{V}$, $f = 1\text{MHz}$, $T_j = 25^\circ\text{C}$

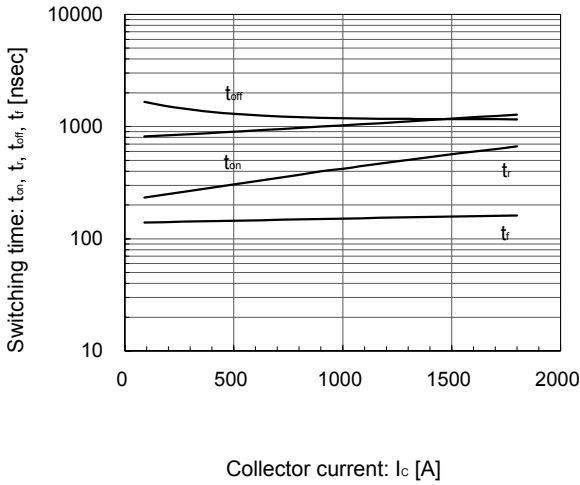


[INVERTER]
Dynamic Gate Charge (typ.)
 $V_{CE} = 600\text{V}$, $I_c = 900\text{A}$, $T_j = 25^\circ\text{C}$



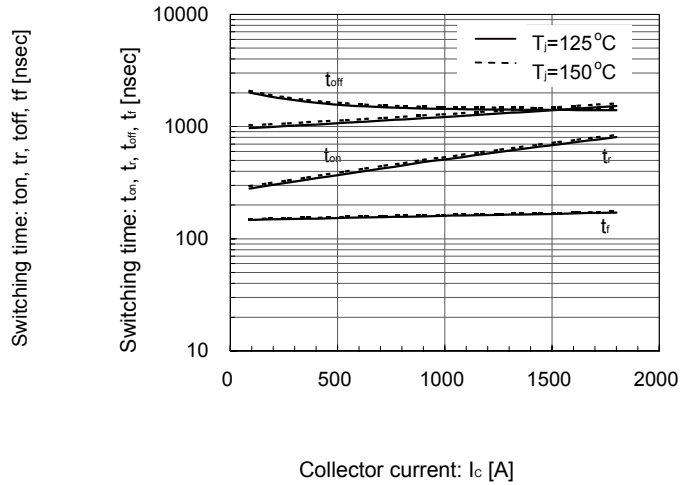
[INVERTER]

Switching time vs. Collector current (typ.)
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=1.6\Omega, T_J=25^\circ C$



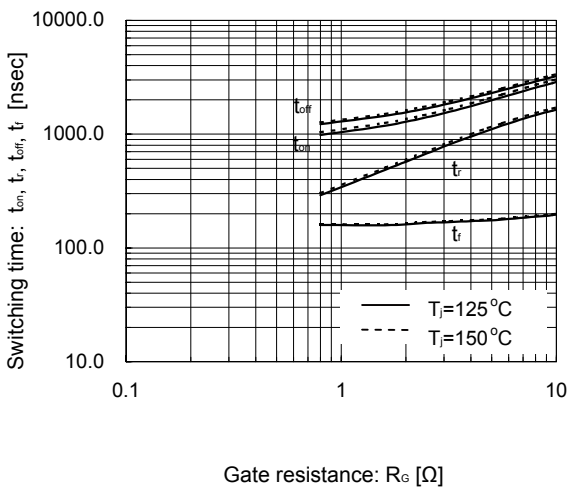
[INVERTER]

Switching time vs. Collector current (typ.)
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=1.6\Omega, T_J=125^\circ C, 150^\circ C$



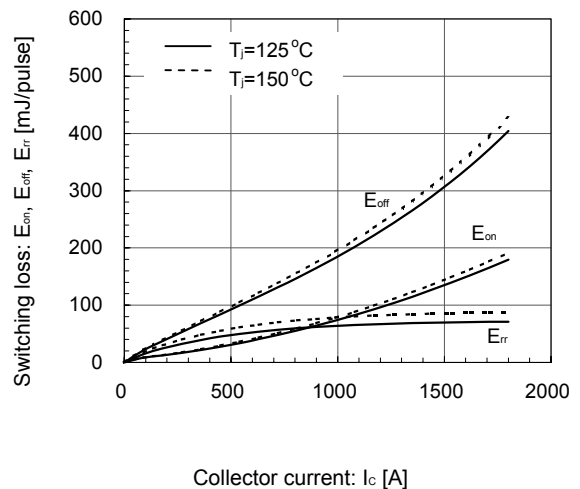
[INVERTER]

Switching time vs. Gate resistance (typ.)
 $V_{CC}=600V, I_c=900A, V_{GE}=\pm 15V, T_J=125^\circ C, 150^\circ C$



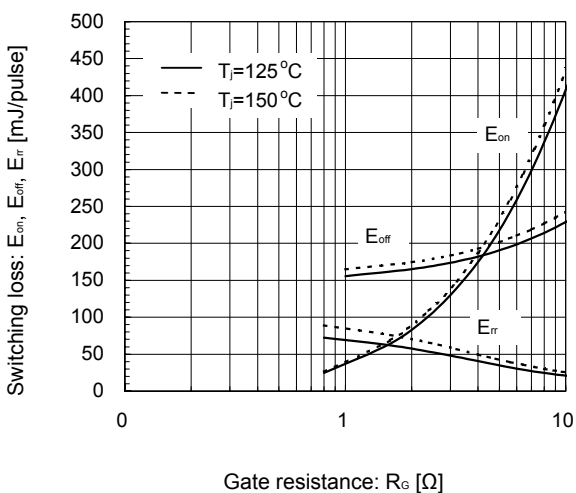
[INVERTER]

Switching loss vs. Collector current (typ.)
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=1.6\Omega, T_J=125^\circ C, 150^\circ C$



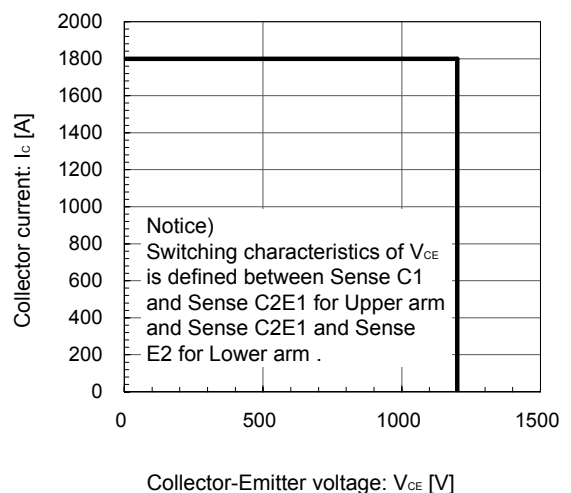
[INVERTER]

Switching loss vs. Gate resistance (typ.)
 $V_{CC}=600V, I_c=900A, V_{GE}=\pm 15V, T_J=125^\circ C, 150^\circ C$



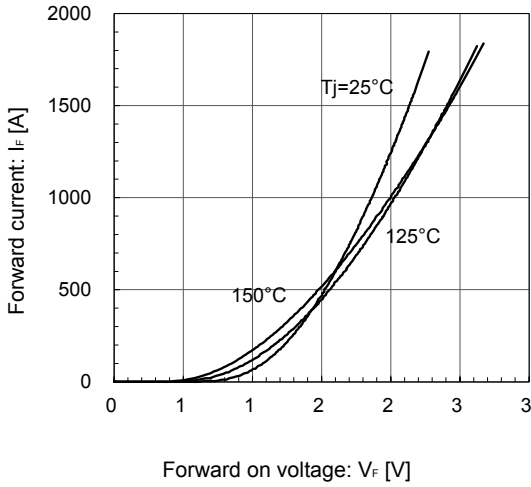
[INVERTER]

Reverse bias safe operating area (max.)
 $+V_{GE}=15V, -V_{GE}=15V, R_G=1.6\Omega, T_J=150^\circ C$



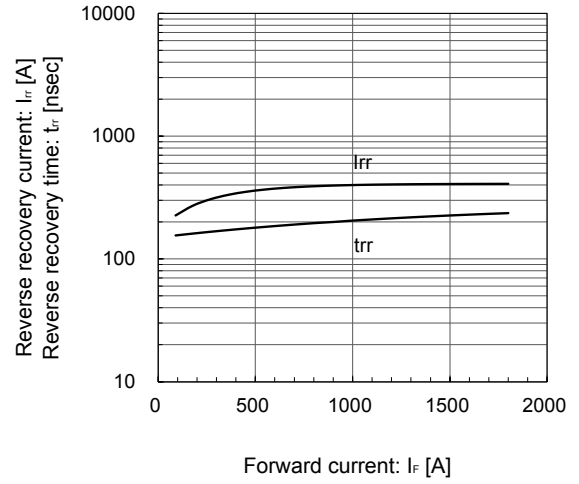
[INVERTER]

Forward Current vs. Forward Voltage (typ.)
chip



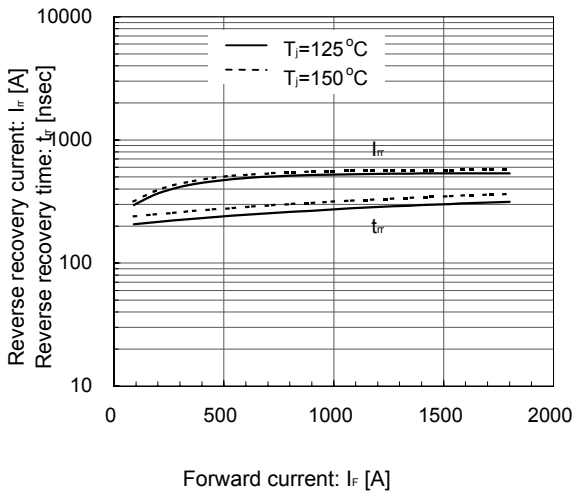
[INVERTER]

Reverse Recovery Characteristics (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_J=25°C

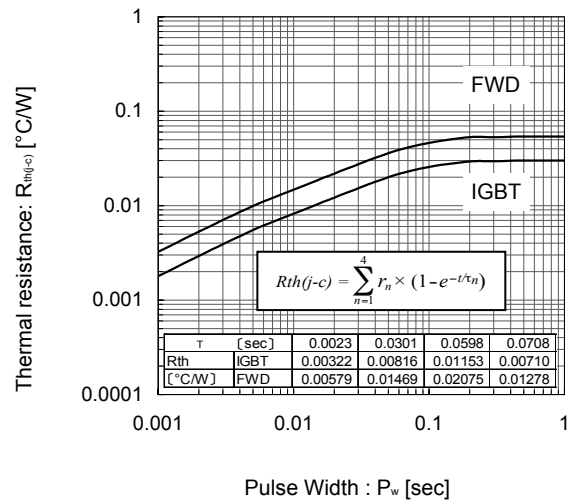


[INVERTER]

Reverse Recovery Characteristics (typ.)
V_{CC}=600V, V_{GE}=±15V, R_G=1.6Ω, T_J=125°C, 150°C

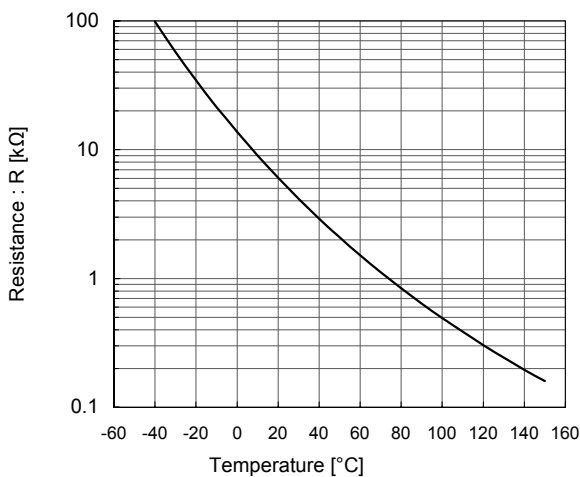


Transient Thermal Resistance (max.)

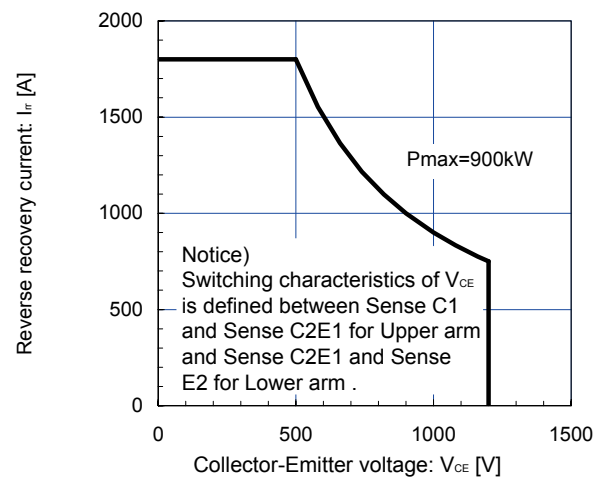


[THERMISTOR]

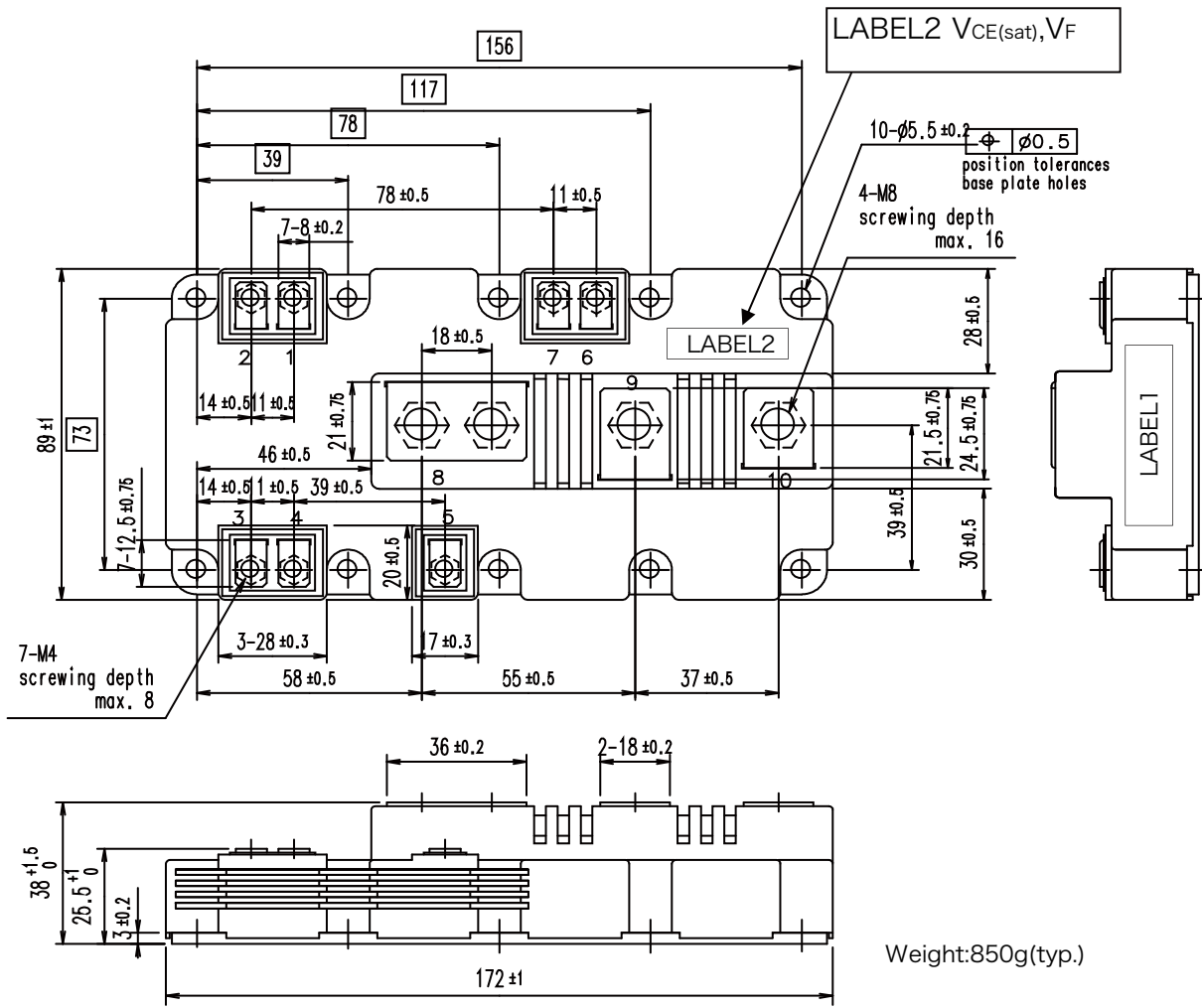
Temperature characteristic (typ.)



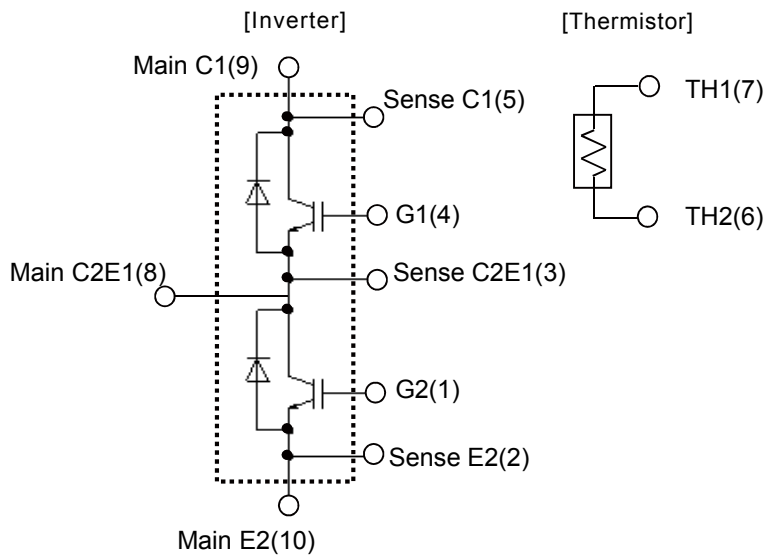
FWD safe operating area (max.)
T_J=150°C



■ Outline Drawings, mm



■ Equivalent Circuit Schematic



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