

Data Sheet Issue:- A1

Provisional Data **Phase Control Thyristor** Types N6012ZD020 to N6012ZD060

Development Type No.: NX373ZD020-060

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V _{DRM}	Repetitive peak off-state voltage, (note 1)	200-600	V
V _{DSM}	Non-repetitive peak off-state voltage, (note 1)	200-600	V
V _{RRM}	Repetitive peak reverse voltage, (note 1)	200-600	V
V _{RSM}	Non-repetitive peak reverse voltage, (note 1)	300-700	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS		
I _{T(AV)M}	Maximum average on-state current, T_{sink} =55°C, (6012	А		
I _{T(AV)M}	Maximum average on-state current. T_{sink} =85°C, (4300	А		
I _{T(AV)M}	Maximum average on-state current. T_{sink} =85°C, (2430	А		
I _{T(RMS)M}	Nominal RMS on-state current, T _{sink} =25°C, (note	11795	А		
I _{T(d.c.)}	D.C. on-state current, T _{sink} =25°C, (note 4)	9310	А		
I _{TSM}	Peak non-repetitive surge t_p =10ms, V_{m} =60% V_{RRI}	65.0	kA		
I _{TSM2}	Peak non-repetitive surge t _p =10ms, V _m ≤10V, (no	71.5	kA		
l²t	$I^{2}t$ capacity for fusing t_{p} =10ms, V_{rm} =60% V_{RRM} , (no	21.13×10 ⁶	A ² s		
l²t	$I^{2}t$ capacity for fusing t _p =10ms, V _m ≤10V, (note 5)	25.56×10 ⁶	A ² s		
(di/dt) _{cr}		(continuous, 50Hz)	100	A/µs	
	Critical rate of rise of on-state current (note 6)	(repetitive, 50Hz, 60s)	200		
		(non-repetitive)	400		
V _{RGM}	Peak reverse gate voltage	5	V		
P _{G(AV)}	Mean forward gate power	4	W		
P _{GM}	Peak forward gate power	30	W		
T _{j op}	Operating temperature range	-40 to +140	°C		
T _{stg}	Storage temperature range	-40 to +150	°C		

Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 140°C T_j initial.
- 6) V_D=67% V_DRM, I_{TM}=2000A, I_{FG}=2A, t_r \!\!\leq\!\! 0.5 \mu s, T_{case} \!\!=\!\! 140^\circ C.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS	
V _{TM}	Maximum peak on-state voltage	-	-	0.95	I _{TM} =4000A	V	
V _{TM}	Maximum peak on-state voltage	-	-	1.45	I _{TM} =20900A	V	
V _{T0}	Threshold voltage	-	-	0.853		V	
r _T	Slope resistance	-	-	0.029		mΩ	
(dv/dt) _{cr}	Critical rate of rise of off-state voltage	1000	-	-	V_D =80% V_{DRM} , linear ramp, gate o/c	V/μs	
I _{DRM}	Peak off-state current	-	-	100	Rated V _{DRM}	mA	
I _{RRM}	Peak reverse current	-	-	100	Rated V _{RRM}	mA	
V _{GT}	Gate trigger voltage	-	-	3.0		V	
I _{GT}	Gate trigger current	-	-	300	$T_j=25^{\circ}C$ $V_D=10V, I_T=3A$	mA	
V_{GD}	Gate non-trigger voltage	-	-	0.25	Rated V _{DRM}	V	
I _H	Holding current	-	-	1000	T _j =25°C	mA	
t _{gd}	Gate-controlled turn-on delay time	-	0.6	1.5	V _D =67% V _{DRM} , I _T =2000A, di/dt=10A/µs,	μs	
t _{gt}	Turn-on time	-	1.0	2.0	I_{FG} =2A, t _r =0.5µs, T _j =25°C	μs	
Q _{rr}	Recovered charge	-	1700	-		μC	
Q _{ra}	Recovered charge, 50% Chord	-	1000	1250	I _{TM} =2000A, t₀=2000µs, di/dt=10A/µs,	μC	
l _{rr}	Reverse recovery current	-	105	-	V _r =100V	А	
t _{rr}	Reverse recovery time, 50% Chord	-	19	-		μs	
t _q	Turn-off time	-	200	-	I _{TM} =2000A, t _p =2000μs, di/dt=10A/μs, V _r =100V, V _{dr} =80%V _{DRM} , dV _{dr} /dt=20V/μs	μs	
		-	250	-	I _{TM} =2000A, t _p =2000µs, di/dt=10A/µs, V _r =100V, V _{dr} =80%V _{DRM} , dV _{dr} /dt=200V/µs	μο	
R _{thJK}	Thermal resistance, junction to heatsink	-	-	0.011	Double side cooled	K/W	
		-	-	0.022	Single side cooled	K/W	
F	Mounting force	36	-	44	Note 2.	kN	
Wt	Weight	-	1.2	-		kg	

Notes:-

1) Unless otherwise indicated $T_j=140^{\circ}C$.

2) For other clamp forces, please consult factory.

Curves



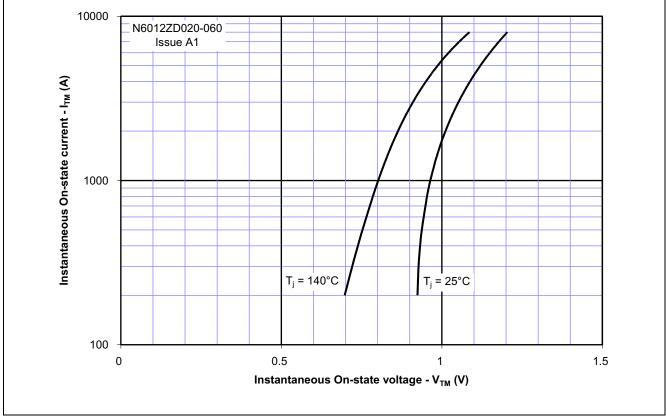
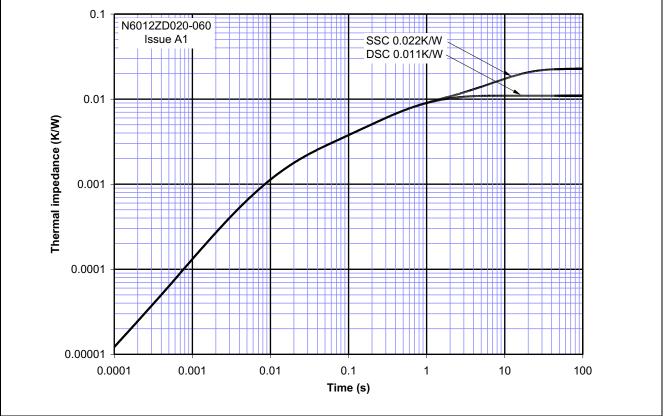
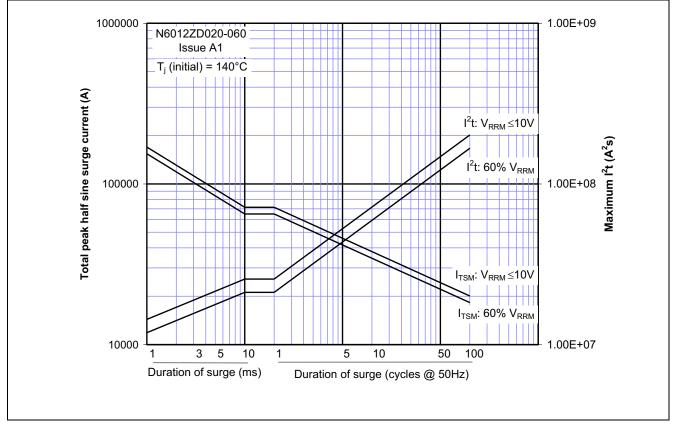


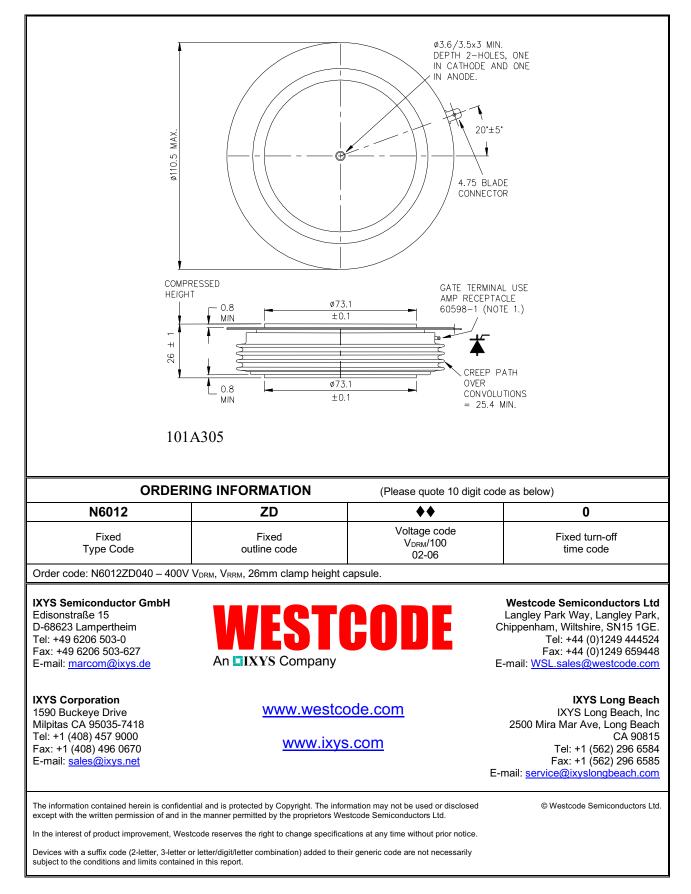
Figure 2 – Transient Thermal Impedance







Outline Drawing & Ordering Information





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