

Date: - 22 Oct, 2003

Data Sheet Issue:- 1

Phase Control Thyristor Types N3839TC300 to N3839TC350

Old Type No.: N1663CH30-35

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	3000-3500	٧
V_{DSM}	Non-repetitive peak off-state voltage, (note 1)	3000-3500	V
V_{RRM}	Repetitive peak reverse voltage, (note 1)	3000-3500	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	3100-3600	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
I _{T(AV)M}	Maximum average on-state current, T _{sink} =55°C, (note 2)	3839	Α
$I_{T(AV)M}$	Maximum average on-state current. T _{sink} =85°C, (note 2)	2668	Α
$I_{T(AV)M}$	Maximum average on-state current. T _{sink} =85°C, (note 3)	1644	Α
I _{T(RMS)M}	Nominal RMS on-state current, T _{sink} =25°C, (note 2)	7527	Α
I _{T(d.c.)}	D.C. on-state current, T _{sink} =25°C, (note 4)	6647	Α
I _{TSM}	Peak non-repetitive surge t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	49.5	kA
I _{TSM2}	Peak non-repetitive surge t _p =10ms, V _{rm} ≤10V, (note 5)	55.0	kA
I ² t	I ² t capacity for fusing t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	12.25×10 ⁶	A ² s
l ² t	I ² t capacity for fusing t _p =10ms, V _{rm} ≤10V, (note 5)	15.13×10 ⁶	A ² s
	Critical rate of rise of on-state current (continuous, 50Hz), (Note 6)	75	A/µs
(di/dt) _{cr}	Critical rate of rise of on-state current (repetitive, 50Hz, 60s), (Note 6)	150	A/µs
	Critical rate of rise of on-state current (non-repetitive), (Note 6)	300	A/µs
V_{RGM}	Peak reverse gate voltage	5	V
P _{G(AV)}	Mean forward gate power	5	W
P_{GM}	Peak forward gate power	50	W
T _{j op}	Operating temperature range	-40 to +125	°C
T _{stg}	Storage temperature range	-40 to +150	°C

Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T_i below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 125°C T_j initial.
- 6) $V_D=67\% \ V_{DRM}, \ I_{GM}=4A, \ t_r\leq 0.5 \mu s, \ T_{case}=125^{\circ}C.$



Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
\/	Maximum neels on etate valtage	-	-	1.74	I _{TM} =6000A	V
V_{TM}	Maximum peak on-state voltage	-	-	2.51	I _{TM} =11500A	V
V _{T0}	Threshold voltage	-	-	0.95		V
r _T	Slope resistance	-	-	0.14		mΩ
(dv/dt) _{cr}	Critical rate of rise of off-state voltage	1000	-	-	V _D =80% V _{DRM} , linear ramp, gate o/c	V/µs
I _{DRM}	Peak off-state current	-	-	250	Rated V _{DRM}	mA
I_{RRM}	Peak reverse current	-	-	250	Rated V _{RRM}	mA
V _{GT}	Gate trigger voltage	-	-	3.0	T _i =25°C V _D =10V, I _T =3A	V
l _{GT}	Gate trigger current	-	-	300	1j-25 C V _D -10V, IT-3A	mA
V_{GD}	Gate non-trigger voltage	-	-	0.25	Rated V _{DRM}	V
I _H	Holding current	-	-	1000	T _j =25°C	mA
t _{gd}	Gate-controlled turn-on delay time	-	1.0	1.5	V _D =67% V _{DRM} , I _T =6000A, di/dt=10A/μs,	μs
t _{gt}	Turn-on time	-	3.0	4.0	I _{FG} =2A, t _r =0.5µs, T _j =25°C	μs
Q _{rr}	Recovered charge	-	13000	-		μC
Q _{ra}	Recovered charge, 50% Chord	-	7800	8500	I _{TM} =4000A, t _p =2000μs, di/dt=10A/μs,	μC
I _{rr}	Reverse recovery current	-	280	-	V _r =100V	Α
t _{rr}	Reverse recovery time	-	55	-		μs
t _q	Turn-off time	-	600	-	I_{TM} =4000A, t_p =2000 μ s, di/dt=10A/ μ s, V_r =100V, V_{dr} =80% V_{DRM} , d V_{dr} /dt=20V/ μ s	μs
rq	Turn-on time	-	1100	-	$ \begin{array}{l} I_{TM}\!\!=\!\!4000A,t_p\!\!=\!\!2000\mu s,di/dt\!\!=\!\!10A/\mu s,\\ V_r\!\!=\!\!100V,V_{dr}\!\!=\!\!80\%V_{DRM},dV_{dr}/dt\!\!=\!\!200V/\mu s \end{array} $	μδ
R_{thJK}	Thermal resistance, junction to heatsink	-	-	0.008	Double side cooled	K/W
i vinjik	Thermal resistance, junction to fleatsink	-	-	0.016	Single side cooled	K/W
F	Mounting force	63	-	77		kN
Wt	Weight	-	1.23	-		kg

Notes:-

1) Unless otherwise indicated T_j=125°C.



Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	$V_{ m DRM} V_{ m DSM} V_{ m RRM} $	V _{RSM} V	V _D V _R DC V
30	3000	3100	1800
32	3200	3300	1920
34	3400	3500	2040
35	3500	3600	2100

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_i below 25°C.

4.0 Repetitive dv/dt

Standard dv/dt is 1000V/µs.

5.0 Snubber Components

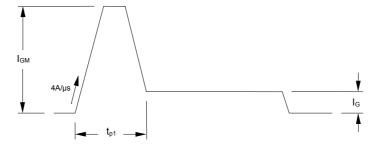
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 300A/µs at any time during turn-on on a non-repetitive basis. For repetitive performance (1 min), the on-state rate of rise of current must not exceed 150A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of I_{GM} should be between five and ten times I_{GT} , which is shown on page 2. Its duration (t_{p1}) should be 20µs or sufficient to allow the anode current to reach ten times I_L , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current I_G should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times I_{GT} .

8.0 Computer Modelling Parameters

8.1 Device Dissipation Calculations

8.1 Device Dissipation Calculations
$$I_{AV} = \frac{-V_0 + \sqrt{V_0^2 + 4 \cdot f f^2 \cdot r_s \cdot W_{AV}}}{2 \cdot f f^2 \cdot r_s} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}} \\ \Delta T = T_{j \max} - T_{Hs}$$

Where V_0 =0.95V, r_s =0.14m Ω ,

 $R_{\it th}$ = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave Double Side Cooled	0.00915	0.00907	0.00898	0.00891	0.00878	0.00864	0.008
Square wave Single Side Cooled	0.01795	0.01781	0.01772	0.01759	0.01731	0.01678	0.016
Sine wave Double Side Cooled	0.00911	0.00903	0.00895	0.00884	0.00867		
Sine wave Single Side Cooled	0.01784	0.01775	0.01763	0.01735	0.01682		

Form Factors							
Conduction Angle 30° 60° 90° 120° 180° 270° d.c.							d.c.
Square wave	3.464	2.449	2	1.732	1.414	1.149	1
Sine wave	3.98	2.778	2.22	1.879	1.57		

8.2 Calculating V_T using ABCD Coefficients

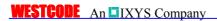
The on-state characteristic I_T vs. V_T, on page 6 is represented in two ways;

- the well established V₀ and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients		125°C Coefficients
Α	1.1	Α	0.9
В	-2.18×10 ⁻¹³	В	1.07×10 ⁻¹³
С	9.00×10 ⁻⁵	С	1.40×10 ⁻⁴
D	2.47×10 ⁻¹⁴	D	-1.04×10 ⁻¹⁴



8.3 D.C. Thermal Impedance Calculation

$$r_{t} = \sum_{p=1}^{p=n} r_{p} \cdot \left(1 - e^{\frac{-t}{\tau_{p}}}\right)$$

Where p = 1 to n, n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r, = Thermal resistance at time t.

 r_p = Amplitude of p_{th} term.

 τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

	D.C. Double Side Cooled						
Term	erm 1 2 3						
r_p	4.293445×10 ⁻³	2.542182×10 ⁻³	1.167986×10 ⁻³				
$ au_{ ho}$	0.8365194	0.176198	0.02000819				

	D.C. Single Side Cooled							
Term	Term 1 2 3 4							
r_p	9.899658×10 ⁻³	2.731068×10 ⁻³	2.619106×10 ⁻³	1.049687×10 ⁻³				
$ au_{p}$	5.623142	1.132481	0.1461472	0.01849926				

9.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1

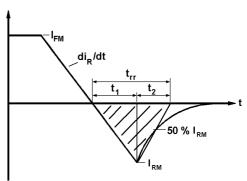


Fig. 1

(ii) Q_{rr} is based on a 150μs integration time i.e.

$$Q_{rr} = \int_{0}^{150\mu s} i_{rr}.dt$$

(ii)
$${
m Q_{rr}}$$
 is based on a 150 μ s integration time i.e.
$$Q_{rr}=\int\limits_0^{150}\!\!\mu_s\,dt$$
 (iii)
$$K\ Factor=\frac{t_1}{t_2}$$

Curves

Figure 1 - On-state characteristics of Limit device

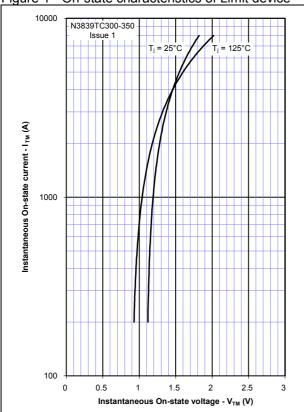


Figure 2 - Transient thermal impedance

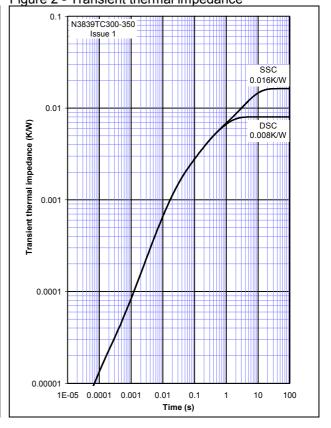


Figure 3 - Gate characteristics - Trigger limits

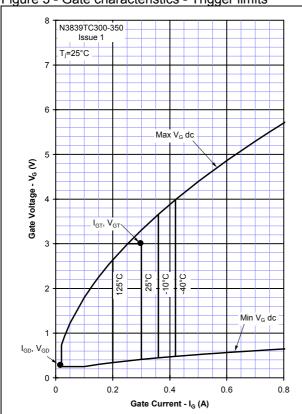
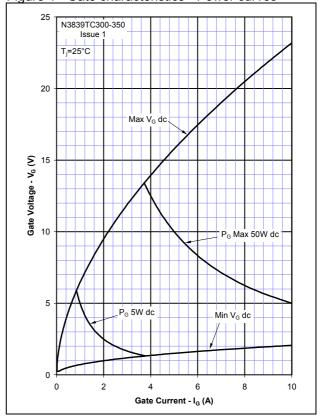


Figure 4 - Gate characteristics - Power curves



1000

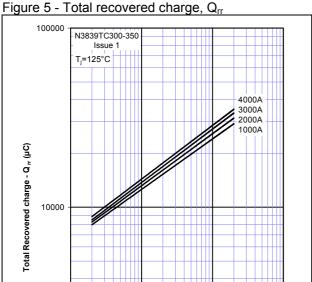


Figure 6 - Recovered charge, Q_{ra} (50% chord)

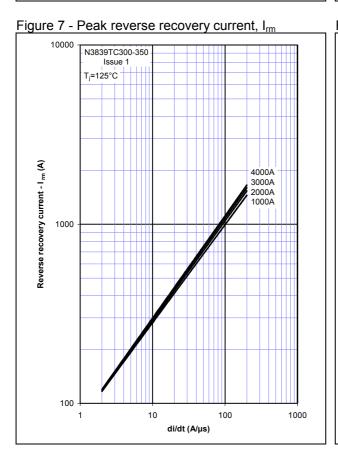
100000

N3839TC300-350
Issue 1
T_j=125°C

4000A
3000A
2000A
1000A

10

1000

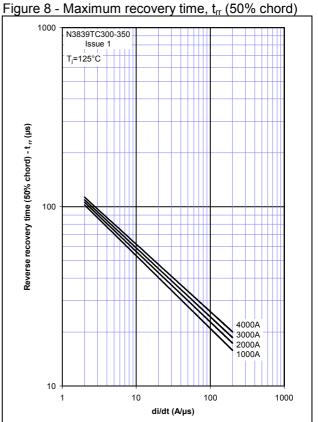


10

di/dt (A/µs)

100

1000



100

di/dt (A/µs)

1000

Figure 9 – On-state current vs. Power dissipation – Double Side Cooled (Sine wave)

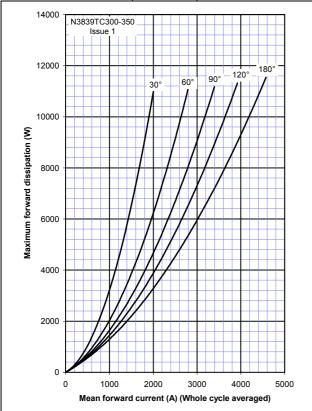


Figure 11 – On-state current vs. Power dissipation – Double Side Cooled (Square wave)

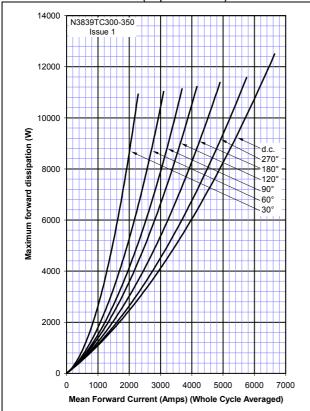


Figure 10 – On-state current vs. Heatsink temperature - Double Side Cooled (Sine wave)

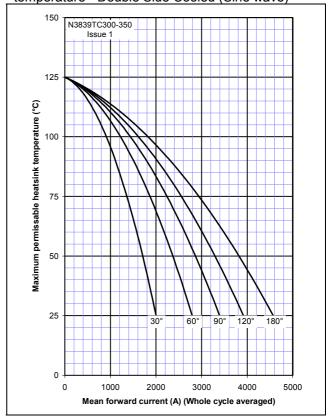


Figure 12 – On-state current vs. Heatsink temperature – Double Side Cooled (Square wave)

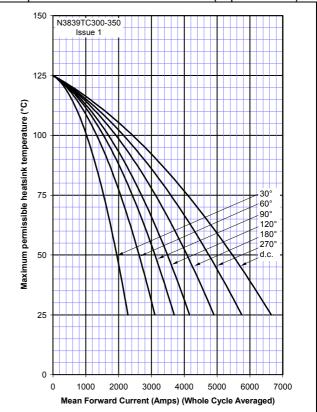


Figure 13 – On-state current vs. Power dissipation – Single Side Cooled (Sine wave)

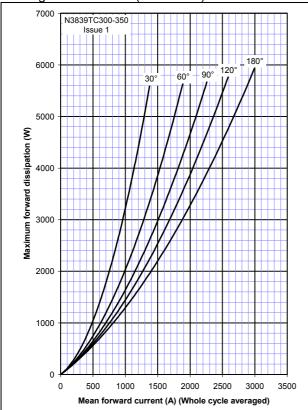


Figure 15 – On-state current vs. Power dissipation – Single Side Cooled (Square wave)

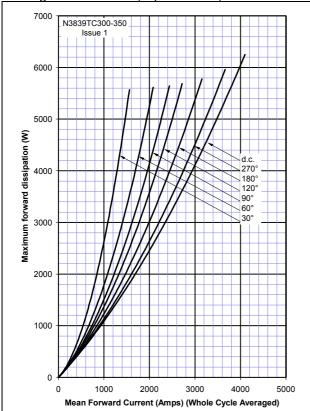


Figure 14 – On-state current vs. Heatsink temperature – Single Side Cooled (Sine wave)

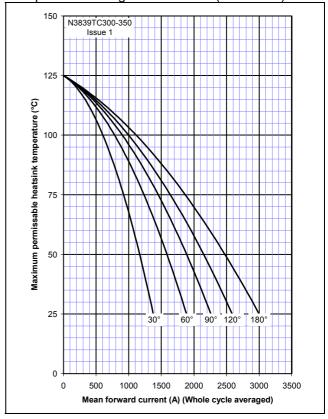
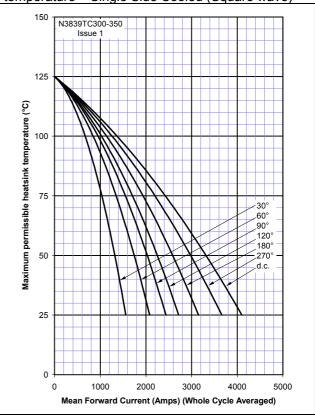
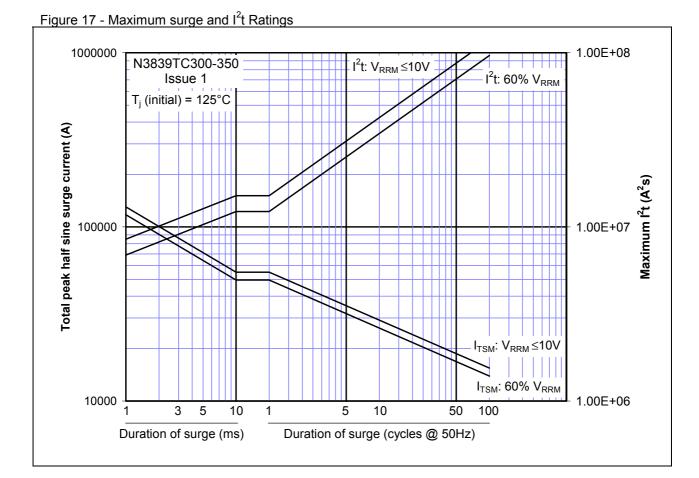


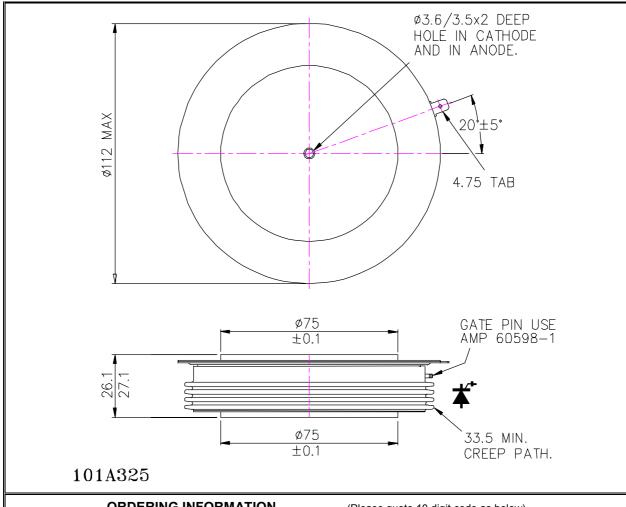
Figure 16 – On-state current vs. Heatsink temperature – Single Side Cooled (Square wave)







Outline Drawing & Ordering Information



ORDERI	NG INFORMATION	(Please quote 10 digit code as below)			
N3839	TC	**	•		
Fixed Type Code	Fixed outline code	Voltage code V _{DRM} /100 30-35	Fixed turn-off time code		

Order code: N3839TC340 – 3400V $V_{\text{DRM}},\,V_{\text{RRM}},\,26.1\text{mm}$ clamp height capsule.

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