IXYS
UK WESTCODE

Advance Data

Wespack Rectifier Diode
Types W5130MK240 to W5130MK280
Development Type No.: WX404MK240-280

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	2400-2800	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	2500-2900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, T_{sink}=55°C, (note 2)	5130	A
$I_{F(AV)M}$	Maximum average forward current. T_{sink}=100°C, (note 2)	3715	A
$I_{F(AV)M}$	Maximum average forward current. T_{sink}=100°C, (note 3)	2080	A
$I_{F(RMS)M}$	Nominal RMS forward current, T_{sink}=25°C, (note 2)	9345	A
$I_{F(d.c.)}$	D.C. forward current, T_{sink}=25°C, (note 4)	7890	A
I_{FSM}	Peak non-repetitive surge t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)	42.0	kA
I_{FSM2}	Peak non-repetitive surge t_p=10ms, V_{rm}≤10V, (note 5)	46.2	kA
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}=60%V_{RRM}, (note 5)	8.82×10^6	A^2s
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}≤10V, (note 5)	10.67×10^6	A^2s
$T_{j\ op}$	Operating temperature range	-40 to +175	°C
T_{stg}	Storage temperature range	-55 to +175	°C

Notes:-
1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 175°C T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{FM}	Maximum peak forward voltage	-	-	1.10	I_{FM}=3000A	V
V_{FM}	Maximum peak forward voltage	-	-	1.98	I_{FM}=16000A	V
V_{T0}	Threshold voltage	-	-	0.91		V
r_T	Slope resistance	-	-	0.07		mΩ
I_{RRM}	Peak reverse current	-	-	75	Rated V_{RRM}	mA
Q_{rr}	Recovered charge	-	5000	5500		µC
Q_{ra}	Recovered charge, 50% Chord	-	3550	-	I_{TM}=1000A, t_p=1000µs, di/dt=10A/µs, V_r=50V	µC
I_{rm}	Reverse recovery current	-	220	-		A
t_{rr}	Reverse recovery time, 50% chord	-	32	-		µs
R_{thJK}	Thermal resistance, junction to heatsink	-	-	0.0130	Double side cooled	K/W
		-	-	0.0240	Anode side cooled	K/W
		-	-	0.0284	Cathode side cooled	K/W
F	Mounting force	25	-	31	Note 2	kN
W_t	Weight		320			g

Notes:-
1) Unless otherwise indicated T_j=175°C.
2) For other clamp forces, please consult factory.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V_{RRM} V	V_{RSM} V	V_R DC V
24	2400	2500	1430
26	2600	2700	1550

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T}$$

and:

$$W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\max} - T_K$$

Where V_{T0}=0.91V, r_T=0.07mΩ,

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave Double Side Cooled	0.01595	0.01492	0.01420	0.01300
Square wave Cathode Side Cooled	0.03182	0.03062	0.02979	0.02840
Sine wave Double Side Cooled	0.01524	0.01429	0.01326	
Sine wave Cathode Side Cooled	0.03099	0.02988	0.02871	

Form Factors				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F, on page 6 is represented in two ways;
(i)   the well established V_{T0} and r_T tangent used for rating purposes and
(ii)   a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients		175°C Coefficients
A	1.051814	A	0.8929391
B	-0.06800862	B	-0.0845593
C	-1.66854×10^{-5}	C	-1.19951×10^{-5}
D	0.01288996	D	0.01682412

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where $p = 1$ to n, n is the number of terms in the series and:

  t = Duration of heating pulse in seconds.
  r_t = Thermal resistance at time t.
  r_p = Amplitude of p_{th} term.
  τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled			
Term	1	2	3
r_p	8.477143×10^{-3}	3.147235×10^{-3}	1.303295×10^{-3}
τ_p	0.3623228	0.09512753	5.059684×10^{-3}

D.C. Cathode Side Cooled			
Term	1	2	3
r_p	0.02049789	6.404445×10^{-3}	1.490447×10^{-3}
τ_p	2.478652	0.1517615	4.891107×10^{-3}

6.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1
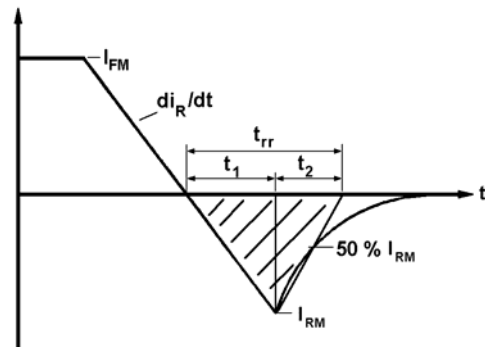
Fig. 1

(ii) Q_{rr} is based on a 150μs integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii)

$$K\ Factor = \frac{t_1}{t_2}$$

Curves

Figure 1 – Forward characteristics of Limit device
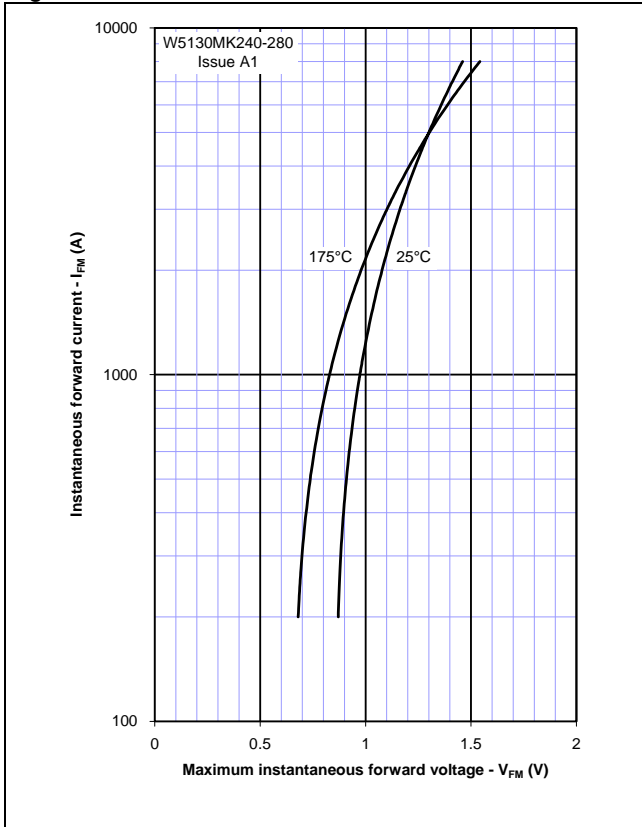
Figure 2 – Transient thermal impedance
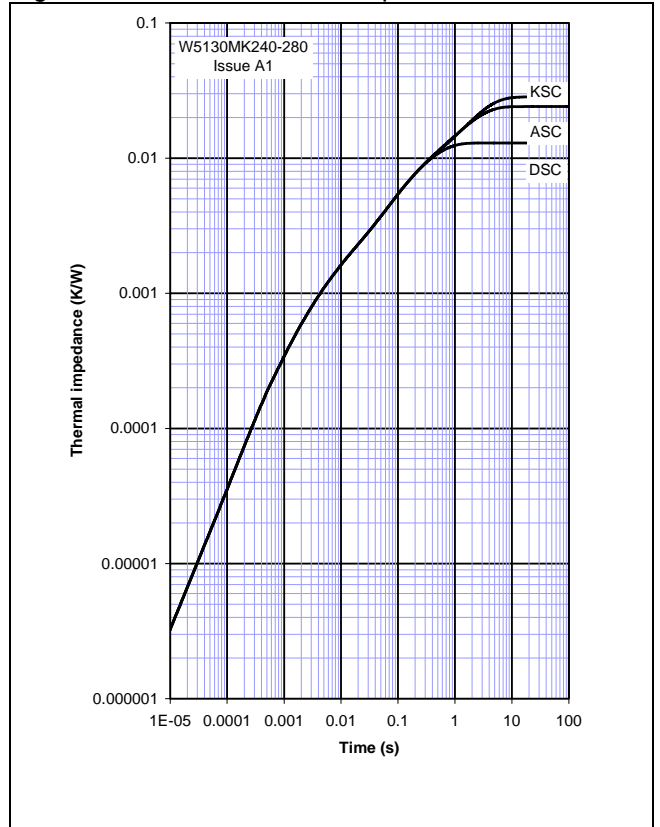
Figure 3 – Maximum Surge Rating
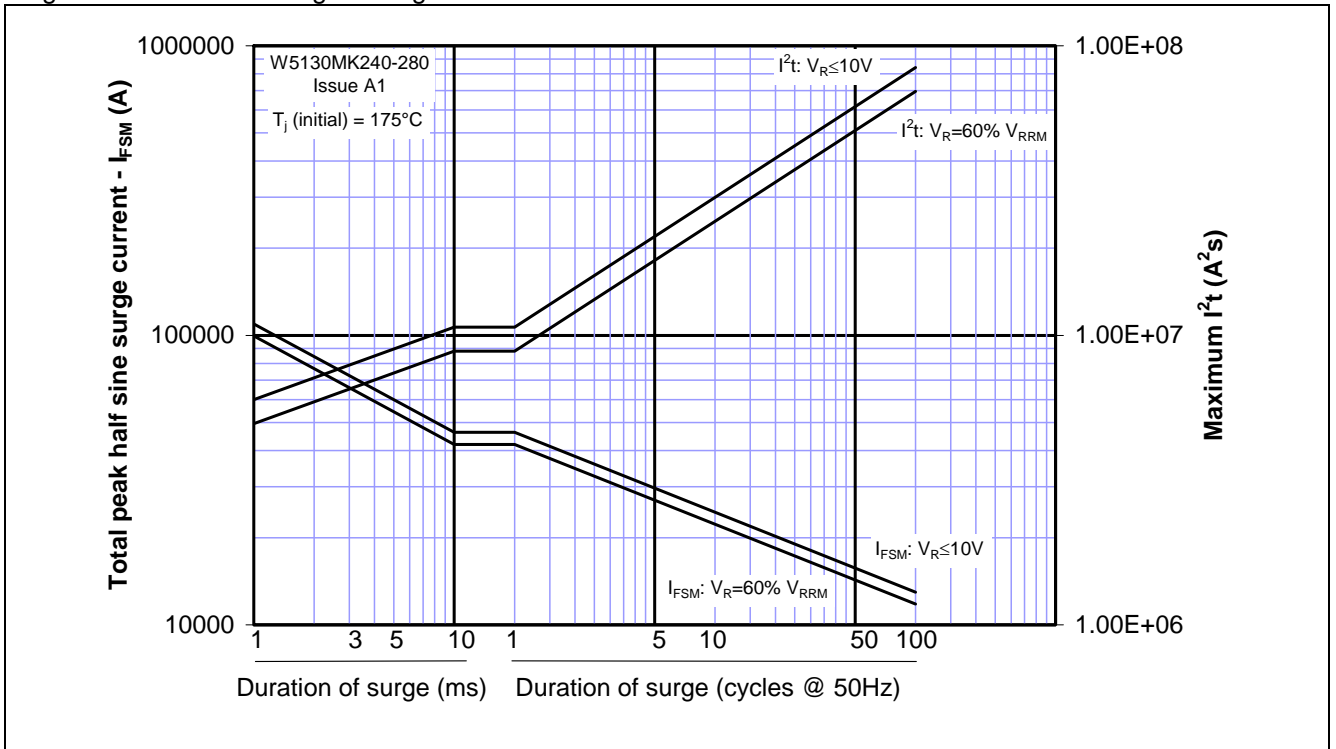
Figure 4 – Total recovered charge, Q_{rr}
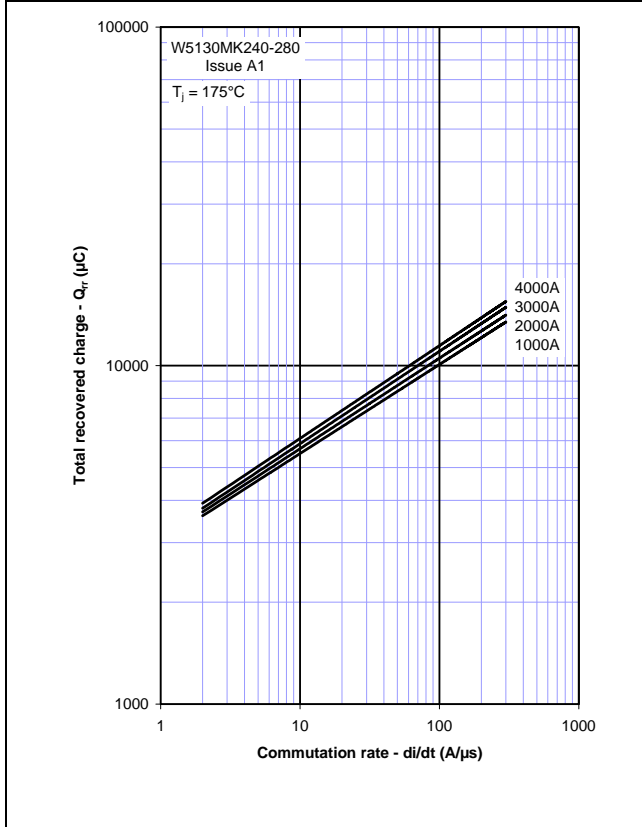
Figure 5 – Recovered charge, Q_{ra} (50% chord)
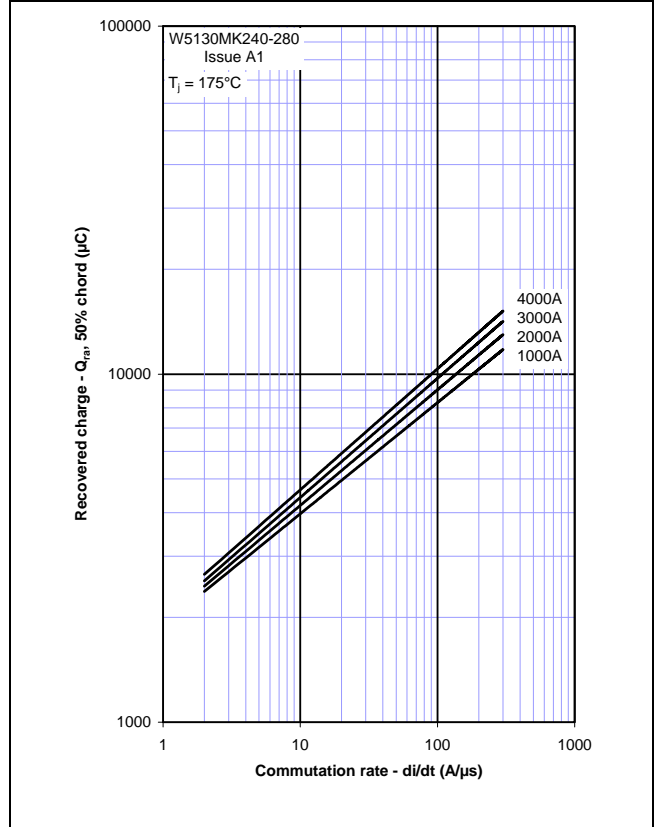
Figure 6 – Peak reverse recovery current, I_{rm}
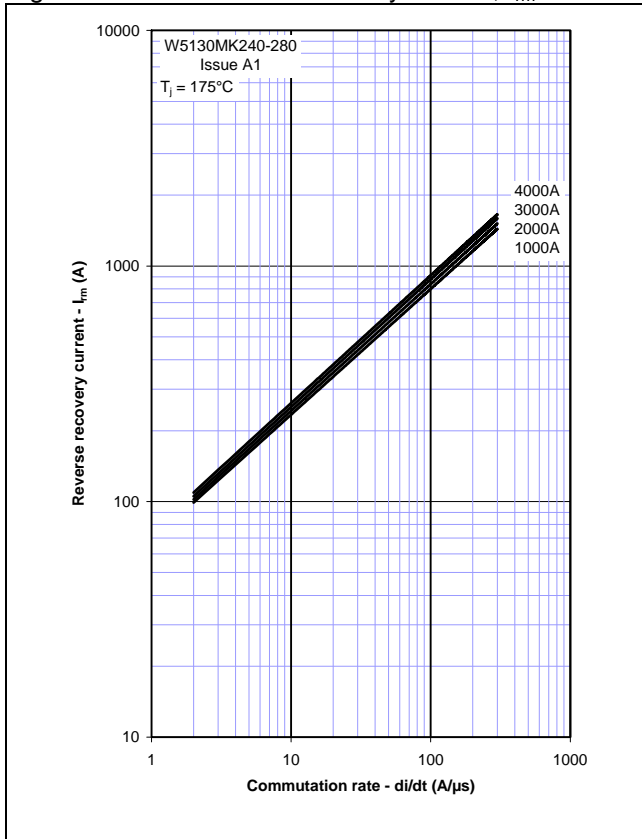
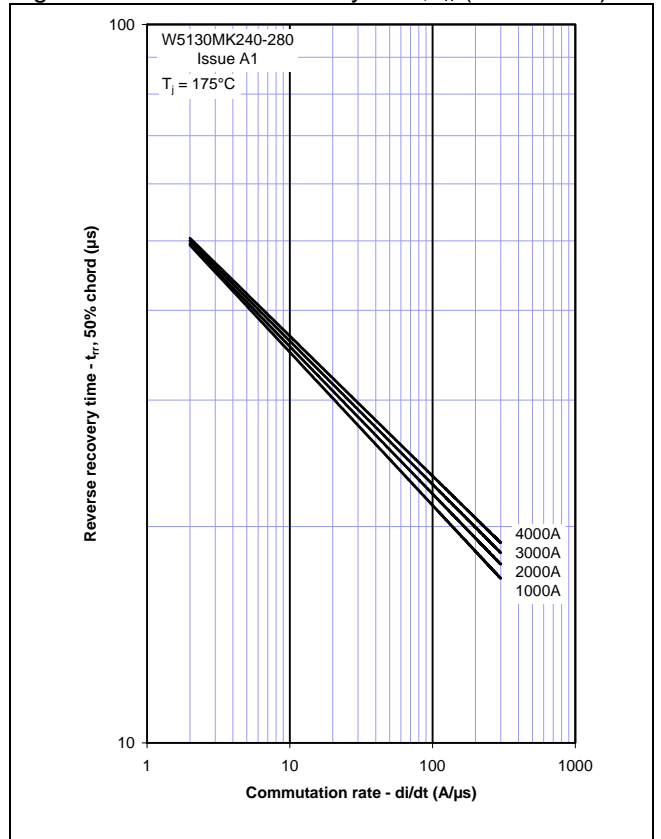Figure 7 – Maximum recovery time, t_{rr} (50% chord)

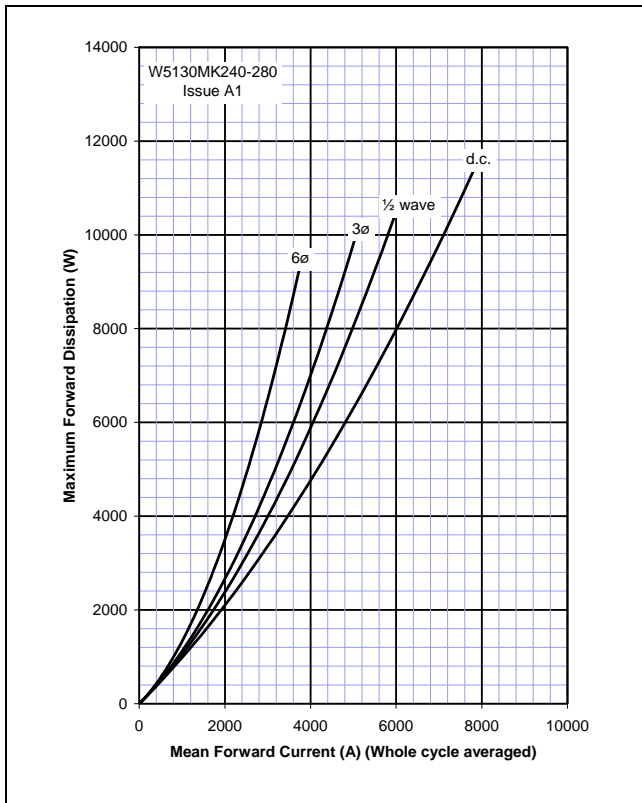Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

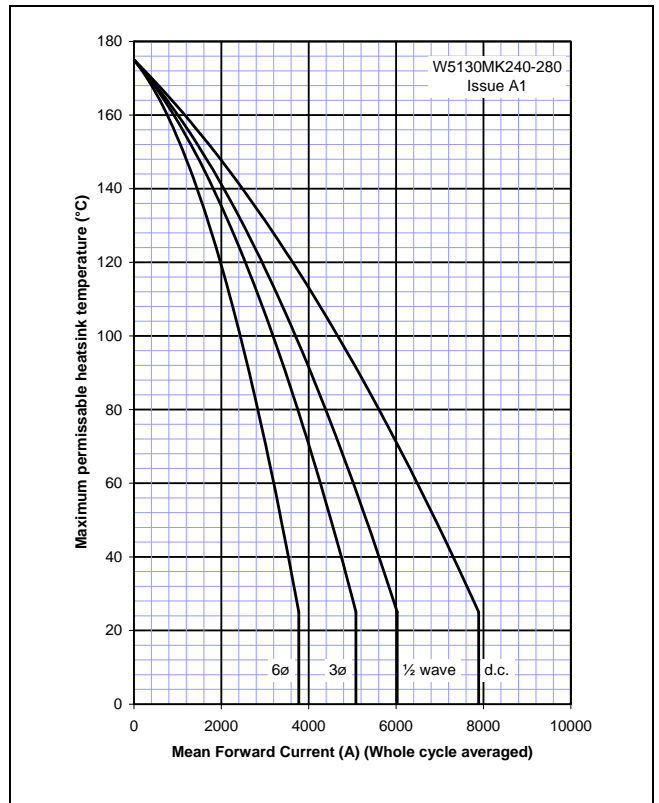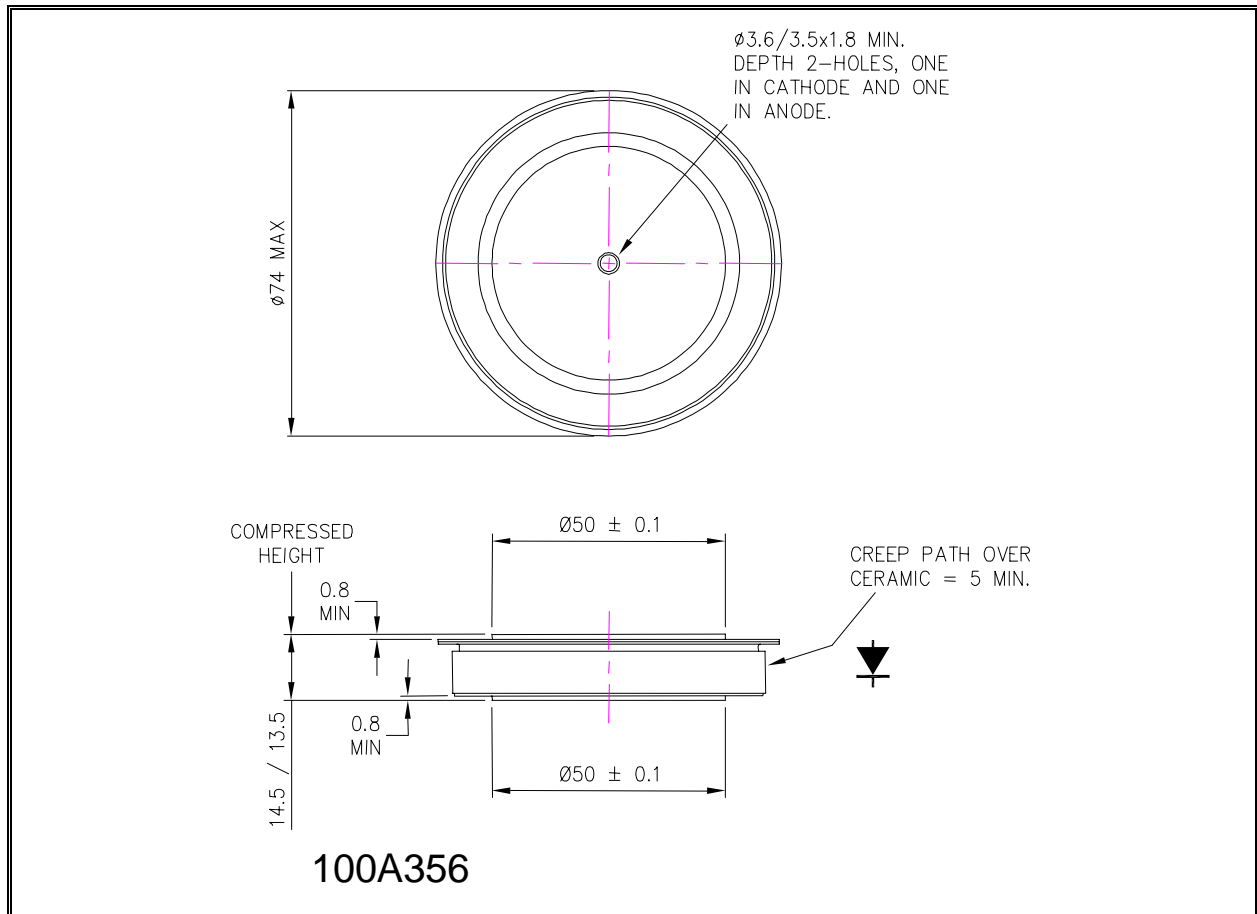

Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled

Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled
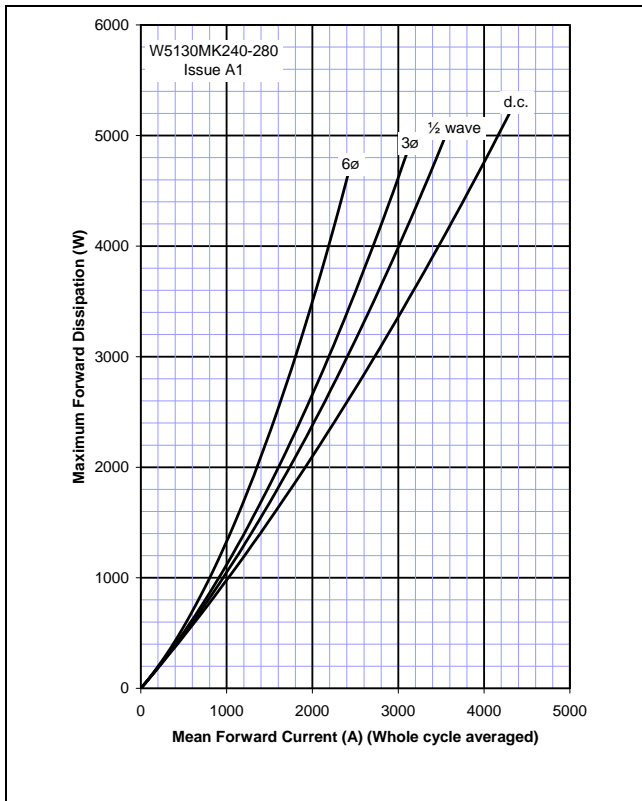
Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled
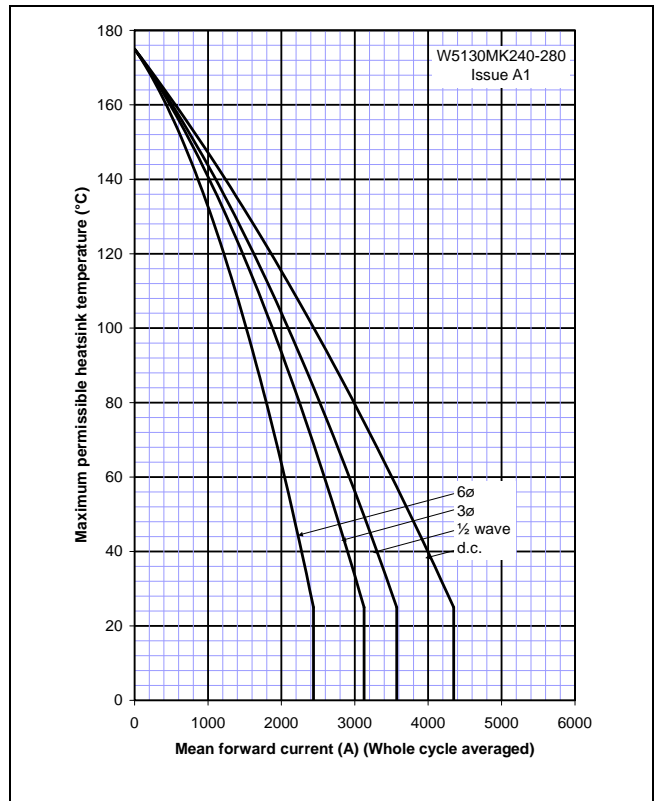
Outline Drawing & Ordering Information

Ø3.6/3.5x1.8 MIN.
DEPTH 2—HOLES, ONE
IN CATHODE AND ONE
IN ANODE.

Ø74 MAX

COMPRESSED HEIGHT

0.8 MIN

0.8 MIN

14.5 / 13.5

Ø50 ± 0.1

Ø50 ± 0.1

CREEP PATH OVER CERAMIC = 5 MIN.

100A356

ORDERING INFORMATION (Please quote 10 digit code as below)

W5130	MK	◆◆	0
Fixed Type Code	Fixed Outline Code	Voltage code $V_{RRM}/100$ 24-28	Fixed code

Order code: W5130MK280 – 2800V V_{RRM}, 14mm clamp height capsule.