## **MDNA700P2200CC**

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 $V_{RRM}$ 

VF

tentative

700 A

1.05 V

= 2x 2200 V

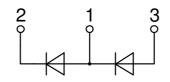
## **Standard Rectifier Module**

Phase I	eg
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Part number MDNA700P2200CC



Backside: isolated **E**72873



### Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- · Very low leakage current

### **Applications:**

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- · Field supply for DC motors

#### Package: ComPack

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Base plate: Copper
  - internally DCB isolated
  - Advanced power cycling
  - Phase Change Material available

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#### Terms and Conditions of Usage

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office. Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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# MDNA700P2200CC

## tentative

Rectifier	•				Rating	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM</sub>	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			2300	V
V <sub>RRM</sub>	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	V
I <sub>R</sub>	reverse current	$V_{R} = 2200 V$	$T_{VJ} = 25^{\circ}C$			500	μA
		$V_{R} = 2200 V$	$T_{vJ} = 150^{\circ}C$			20	mA
V <sub>F</sub>	forward voltage drop	I <sub>F</sub> = 700 A	$T_{VJ} = 25^{\circ}C$			1.14	V
		I <sub>F</sub> = 1400 A				1.35	V
		$I_{F} = 700 \text{ A}$	T <sub>vJ</sub> = 125 °C			1.05	V
		$I_{F} = 1400 \text{ A}$				1.30	V
FAV	average forward current	T <sub>c</sub> = 100°C	$T_{vJ} = 150 ^{\circ}C$			700	Α
		rectangular d = 0.5					
V <sub>F0</sub>	threshold voltage $T_{vJ} = 150^{\circ}$		T <sub>vJ</sub> = 150°C			0.78	V
r <sub>F</sub>	slope resistance } for power	loss calculation only				0.35	mΩ
R <sub>thJC</sub>	thermal resistance junction to ca	ase				0.055	K/W
R <sub>thCH</sub>	thermal resistance case to heats	sink			0.02		K/W
P <sub>tot</sub>	total power dissipation		$T_c = 25^{\circ}C$			2270	W
I <sub>FSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{vJ} = 45^{\circ}C$			20.0	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			21.6	kA
		t = 10 ms; (50 Hz), sine	T <sub>vj</sub> = 150°C			17.0	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			18.4	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{vJ} = 45^{\circ}C$			2.00	MA <sup>2</sup> s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.94	MA <sup>2</sup> s
		t = 10 ms; (50 Hz), sine	$T_{vJ} = 150 ^{\circ}\text{C}$			1.45	MA <sup>2</sup> s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.40	MA <sup>2</sup> s
C	junction capacitance	V <sub>R</sub> = 400 V; f = 1 MHz	$T_{vJ} = 25^{\circ}C$		781		pF

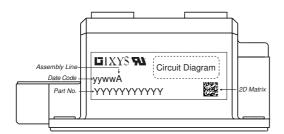
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Package ComPack					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
	RMS current	per terminal				1200	Α	
T <sub>vj</sub>	virtual junction temperature			-40		150	°C	
T <sub>op</sub>	operation temperature					125	°C	
T <sub>stg</sub>	storage temperature					125	°C	
Weight					500		g	
M <sub>D</sub>	mounting torque			3		5	Nm	
M <sub>T</sub>	terminal torque			12		14	Nm	
d <sub>Spp/App</sub>		ing distance through air	terminal to terminal	21.0			mm	
d <sub>Spb/Apb</sub>	creepage distance on surface   striking distance ti		terminal to backside	18.0			mm	
	isolation voltage t = 1 second t = 1 minute	t = 1 second	50/60 Hz, RMS; IIso∟ ≤ 1 mA	4800			V	
		t = 1 minute		4000			V	



#### Part description

M = Module

D = Diode N = High Voltage Standard Rectifier

A = (>= 2000V) 700 = Current Rating [A]

P = Phase leg 2200 = Reverse Voltage [V]

CC = ComPack

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDNA700P2200CC	MDNA700P2200CC	Box	3	

Equivalent Circuits for Simulation			* on die level	T <sub>vJ</sub> = 150 °C
	- Ro-	Rectifier		
V <sub>0 max</sub>	threshold voltage	0.78		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	0.16		mΩ

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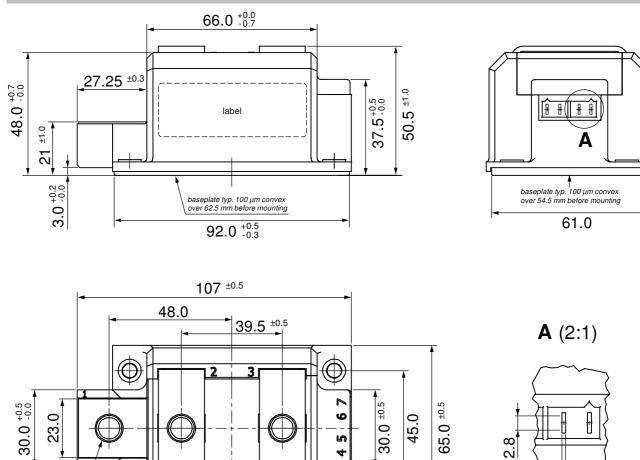
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ComPack

Outlines

# MDNA700P2200CC

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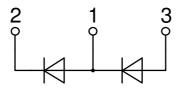
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18.0

80,0

93,5





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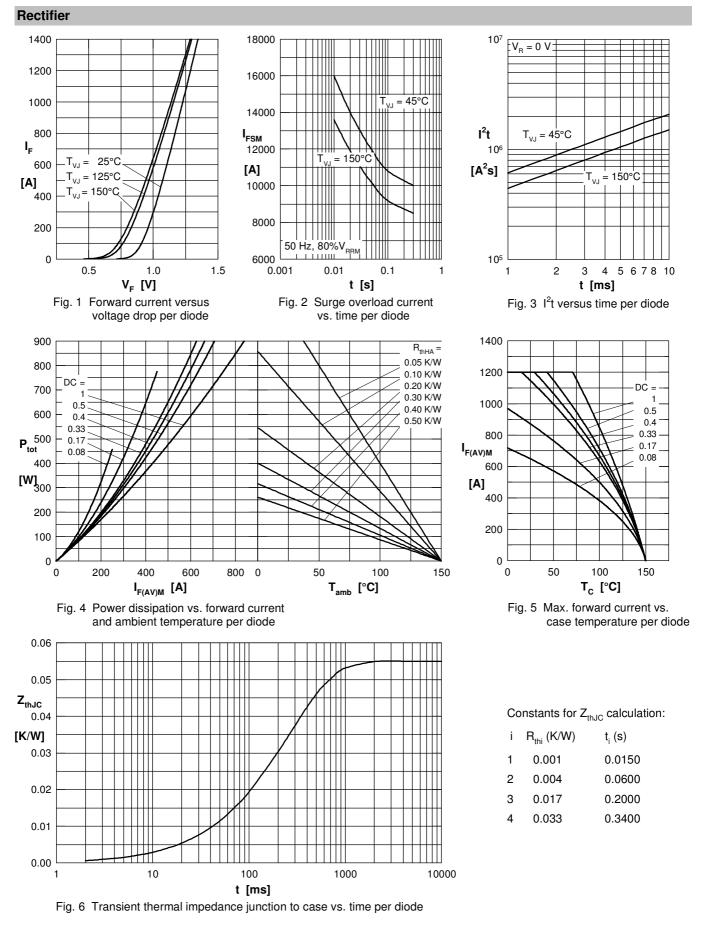
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M10x16 (3x)

05.5

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