### **MDNA85P2200TG**

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### **Standard Rectifier Module**

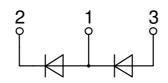
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Part number **MDNA85P2200TG** 



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Backside: isolated **E**72873



### Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- · Very low leakage current

### **Applications:**

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- · Field supply for DC motors

#### Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

#### Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you. Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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$V_{\text{RRM}}$	= 2x 2200 V		
I <sub>FAV</sub>	=	85 A	

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1.1 V

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Rectifier	·				Rating	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM</sub>	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			2300	V
V <sub>RRM</sub>	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	V
I <sub>R</sub>	reverse current	$V_{R}$ = 2200 V	$T_{vJ} = 25^{\circ}C$			100	μA
		$V_{R}$ = 2200 V	$T_{vJ} = 150^{\circ}C$			2	mA
V <sub>F</sub>	forward voltage drop	I <sub>F</sub> = 85 A	$T_{VJ} = 25^{\circ}C$			1.15	V
		I <sub>F</sub> = 170 A				1.38	V
V <sub>F0</sub> r <sub>F</sub> R <sub>thJC</sub>		I <sub>F</sub> = 85 A	T <sub>vJ</sub> = 125 °C			1.10	V
		$I_{F} = 170 \text{ A}$				1.39	V
FAV	average forward current	$T_c = 100^{\circ}C$	$T_{vJ} = 150 ^{\circ}C$			85	Α
		rectangular d = 0.5					
V <sub>F0</sub>	threshold voltage		$T_{VJ} = 150 ^{\circ}C$			0.79	V
r <sub>F</sub>	slope resistance } for power	loss calculation only				3.5	mΩ
R <sub>thJC</sub>	thermal resistance junction to ca	ase				0.35	K/W
R <sub>thCH</sub>	thermal resistance case to heat	sink			0.20		K/W
P <sub>tot</sub>	total power dissipation		$T_c = 25^{\circ}C$			350	W
IFSM	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			1.50	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.62	kA
		t = 10 ms; (50 Hz), sine	T <sub>vJ</sub> = 150°C			1.28	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.38	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			11.3	kA <sup>2</sup> s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			10.9	kA²s
		t = 10 ms; (50 Hz), sine	$T_{vJ} = 150 ^{\circ}\text{C}$			8.13	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			7.87	kA²s
C	junction capacitance	$V_{R}$ = 400 V; f = 1 MHz	$T_{vJ} = 25^{\circ}C$		60		pF

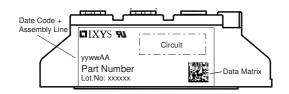
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Package TO-240AA					Ratings			
Symbol	Definition	Conditions			min.	typ.	max.	Unit
	RMS current	per terminal					200	Α
T <sub>vj</sub>	virtual junction temperature	)			-40		150	°C
T <sub>op</sub>	operation temperature				-40		125	°C
T <sub>stg</sub>	storage temperature				-40		125	°C
Weight						76		g
M <sub>D</sub>	mounting torque				2.5		4	Nm
M <sub>T</sub>	terminal torque				2.5		4	Nm
d <sub>Spp/App</sub>	creepage distance on surface   striking distance throu		terminal to terminal	13.0	9.7			mm
<b>d</b> <sub>Spb/Apb</sub>	creepage distance on suna	ice   suiking distance unough an	terminal to backside	16.0	16.0			mm
V	isolation voltage	t = 1 second			4800			V
	t = 1 mir		50/60 Hz, RMS; liso∟ ≤ 1 mA		4000			V



#### Part description

M = Module

D = Diode N = High Voltage Standard Rectifier

N = High Voltage Standar A = (>= 2000V) 85 = Current Rating [A] P = Phase leg 2200 = Reverse Voltage [V] TG = TO-240AA

[	Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
	Standard	MDNA85P2200TG	MDNA85P2200TG	Box	36	

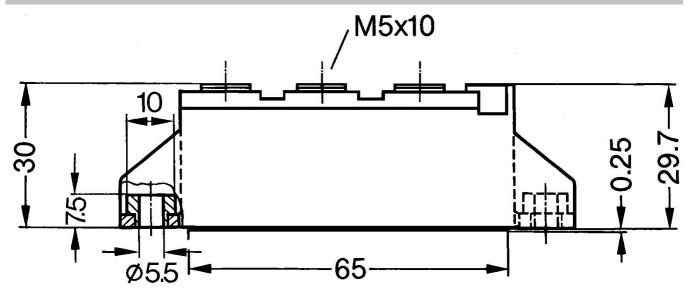
Equiva	alent Circuits for	Simulation	* on die level	T <sub>vj</sub> = 150 °C
	- Ro-	Rectifier		
V <sub>0 max</sub>	threshold voltage	0.79		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	2.3		mΩ

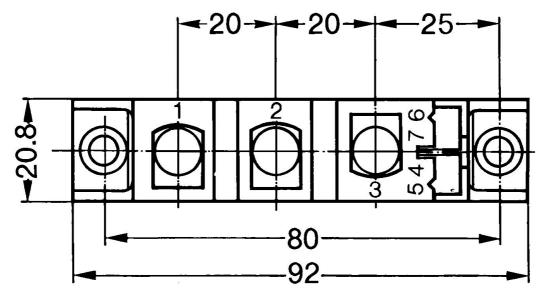
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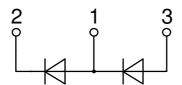
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## **MDNA85P2200TG**

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DC =

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0.5

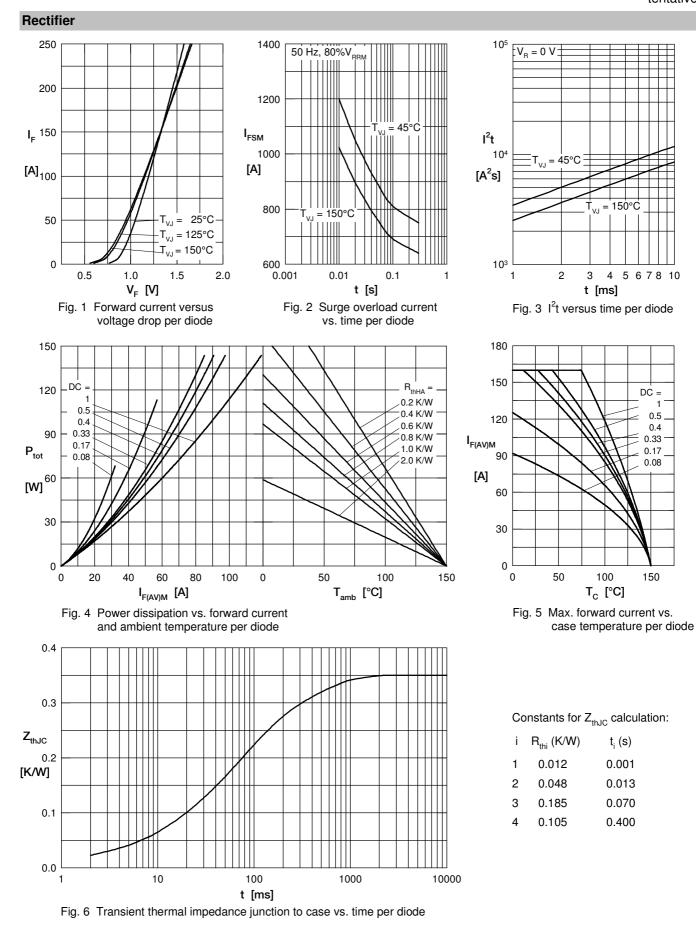
0.4

0.33

0.17

0.08

150





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