

Standard Rectifier Module

= 2x 1200 V

110 A

V_E 1.14 V

Phase leg

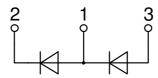
Part number

MDMA110P1200TG



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

20161222c



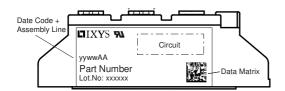


Rectifier					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1300	V	
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1200	V	
I _R	reverse current	V _R = 1200 V	$T_{VJ} = 25^{\circ}C$			100	μΑ	
		$V_R = 1200 \text{ V}$	$T_{VJ} = 150$ °C			2	mΑ	
V _F	forward voltage drop	I _F = 110 A	$T_{VJ} = 25^{\circ}C$			1.21	V	
		$I_F = 220 \text{ A}$				1.44	٧	
		$I_F = 110 \text{ A}$	T _{VJ} = 125°C			1.14	V	
		$I_F = 220 \text{ A}$				1.44	٧	
I _{FAV}	average forward current	T _C = 100°C	$T_{VJ} = 150$ °C			110	Α	
		rectangular d = 0.5						
V _{F0}	threshold voltage		$T_{VJ} = 150$ °C			0.82	٧	
\mathbf{r}_{F}	slope resistance \(\) for power	loss calculation only				2.8	mΩ	
R _{thJC}	thermal resistance junction to ca	ase				0.3	K/W	
R _{thCH}	thermal resistance case to heats	sink			0.20		K/W	
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			415	W	
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			2.00	kA	
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			2.16	kA	
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			1.70	kA	
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.84	kA	
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			20.0	kA2s	
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			19.4	kA2s	
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			14.5	kA2s	
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			14.0	kA2s	
C¹	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		73		pF	
	•			+	 	I		



MDMA110P1200TG

Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperature	Э			-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{\scriptscriptstyleT}$	terminal torque				2.5		4	Nm
d _{Spp/App}	creepage distance on surface striking distance through air terminal to terminal to backside		13.0	9.7			mm	
$d_{\text{Spb/Apb}}$			terminal to backside	16.0	16.0			mm
V	isolation voltage	isolation voltage t = 1 second		•	4800			٧
		t = 1 minute	50/60 Hz, RMS; lisoL ≤ 1 mA		4000			٧



Part description

M = Module

D = Diode
M = Standard Rectifier

A = (up to 1800V) 110 = Current Rating [A]

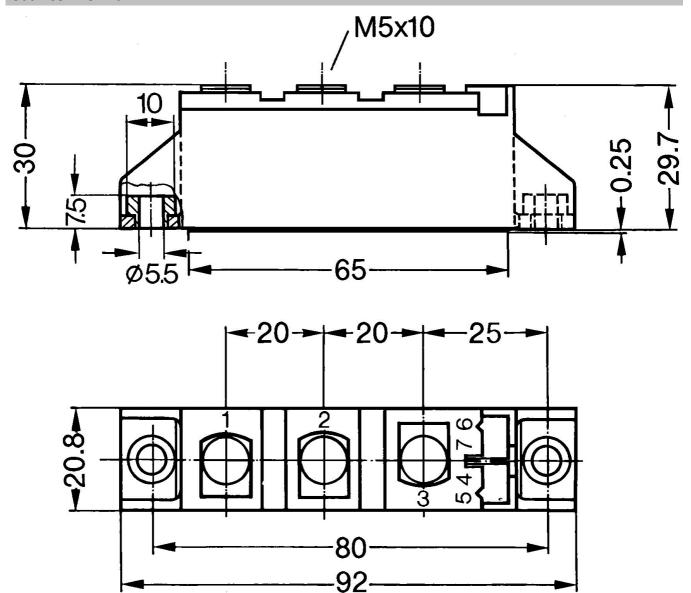
P = Phase leg 1200 = Reverse Voltage [V] TG = TO-240AA

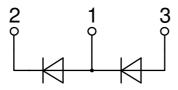
0	rdering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
S	standard	MDMA110P1200TG	MDMA110P1200TG	Box	36	514304

Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 150 ^{\circ}\text{C}$
$I \rightarrow V_0$	R _o -	Rectifier		
V _{0 max}	threshold voltage	0.82		V
$R_{0 \; \text{max}}$	slope resistance *	1.6		$m\Omega$



Outlines TO-240AA







Rectifier

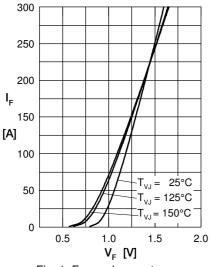


Fig. 1 Forward current versus voltage drop per diode

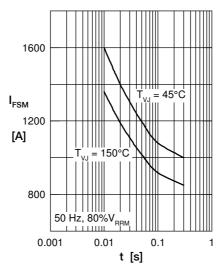


Fig. 2 Surge overload current vs. time per diode

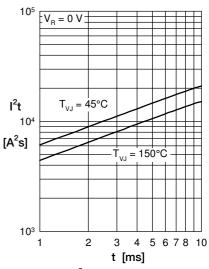


Fig. 3 I²t versus time per diode

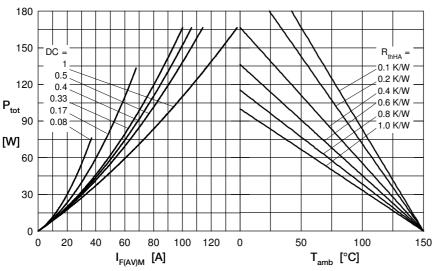


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

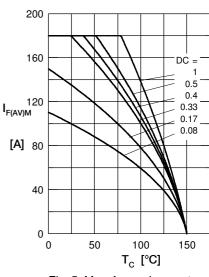


Fig. 5 Max. forward current vs. case temperature per diode

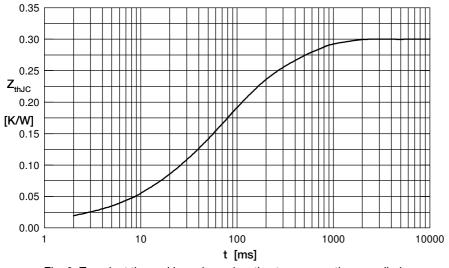


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	R_{thi} (K/W)	t _i (s)
1	0.01	0.001
2	0.04	0.013
3	0.16	0.070
4	0.09	0.400